

### • General Description

AP4411 combines advanced MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . This device is most suitable to load-switch or PWM applications.

### • Applications

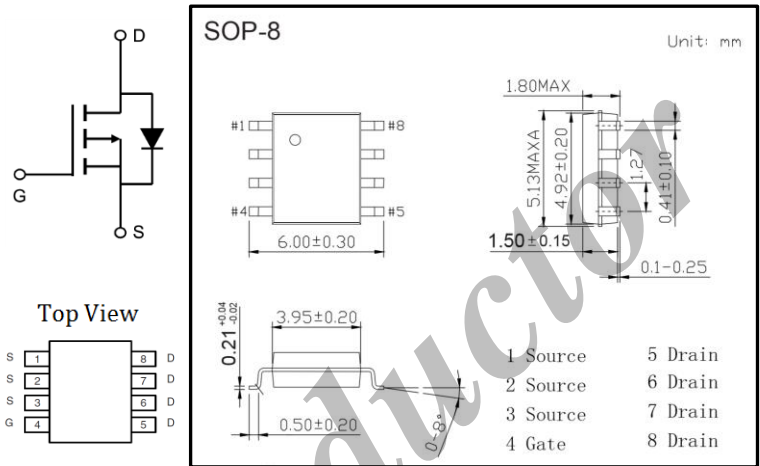
- DC-DC converter for portable devices
- Load switch

### • Product Summary

$V_{DS}$	30V
$I_D$ (at $V_{GS} = -10V$ )	8A
$R_{DS(ON)}$ (at $V_{GS} = -10V$ )	< 32m $\Omega$
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$ )	< 55m $\Omega$

### • Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_a = 25^\circ C$	-8
		$T_a = 70^\circ C$	-6.6
Pulsed Drain Current	$I_{DM}$	-40	A
Avalanche Current	$I_{AS}, I_{AR}$	23	
Avalanche Energy ( $L = 0.1mH$ )	$E_{AS}, E_{AR}$	26	mJ
Power Dissipation	$P_D$	$T_a = 25^\circ C$	3.1
		$T_a = 70^\circ C$	2
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$
Thermal Characteristics			
Thermal Resistance. Junction-to-Ambient	$R_{\theta JA}$	$t \leq 10s$	40
		Steady State	75
Thermal Resistance. Junction-to-Lead	$R_{\theta JL}$	24	$^\circ C/W$



• **Electrical Characteristics Ta = 25°C**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
<b>Static Parameters</b>							
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D = -250\mu A, V_{GS} = 0V$	-30			V	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -30V, V_{GS} = 0V$			-1	$\mu A$	
		$V_{DS} = -30V, V_{GS} = 0V, T_j = 55^\circ C$			-5		
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.3	-1.85	-2.4	V	
On-State Drain Current	$I_{D(ON)}$	$V_{GS} = -10V, V_{DS} = -5V$	-40			A	
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -8A$		21	32	m $\Omega$	
		$V_{GS} = -10V, I_D = -8A, T_j = 125^\circ C$		31.5	38		
		$V_{GS} = -4.5V, I_D = -5A$		33	55		
Forward Transconductance	$g_{FS}$	$V_{DS} = -5V, I_D = -8A$		19		S	
Diode Forward Voltage	$V_{SD}$	$I_S = -1A, V_{GS} = 0V$		-0.8	-1	V	
Maximum Body-Diode Continuous Current	$I_S$				-3.5	A	
<b>Dynamic Parameters</b>							
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$		760		pF	
Output Capacitance	$C_{oss}$			140			
Reverse Transfer Capacitance	$C_{rss}$			95			
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$	1.5	3.2	5	$\Omega$	
<b>Switching Parameters</b>							
Total Gate Charge (10V)	$Q_g$	$V_{GS} = -10V, V_{DS} = -15V, I_D = -8A$		13.6	16	nC	
Total Gate Charge (4.5V)				6.7	8		
Gate Source Charge			$Q_{gs}$		2.5		
Gate Drain Charge			$Q_{gd}$		3.2		
Turn-On Delay Time	$t_{D(on)}$	$V_{GS} = -10V, V_{DS} = -15V, R_L = 1.8\Omega, R_{GEN} = 3\Omega$		8		ns	
Turn-On Rise Time	$t_r$			6			
Turn-Off Delay Time	$t_{D(off)}$			17			
Turn-Off Fall Time	$t_f$			5			
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = -8A, di/dt = 100A/\mu s$		15			
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = -8A, di/dt = 100A/\mu s$		9.7		nC	

• **Ordering Information**

Ordering Part Number	Package	MOQ
AP4411	SOP-8	2,500 pcs / reel

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• Typical Electrical and Thermal Characteristics

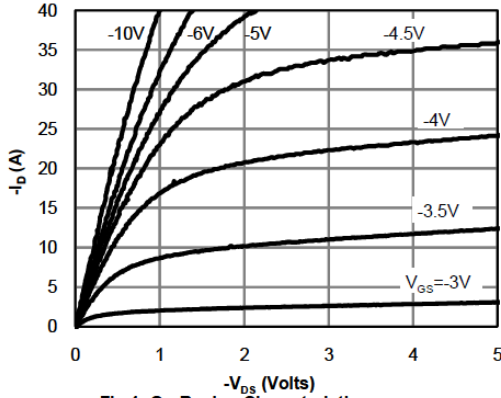


Fig 1: On-Region Characteristics

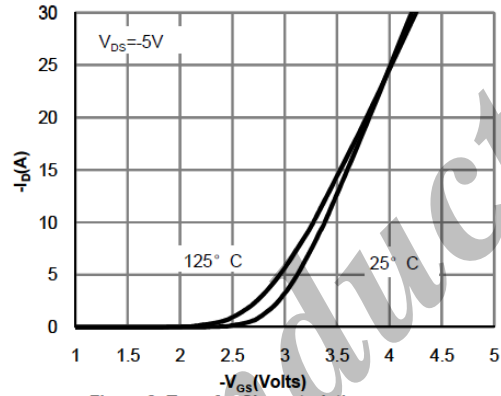


Figure 2: Transfer Characteristics

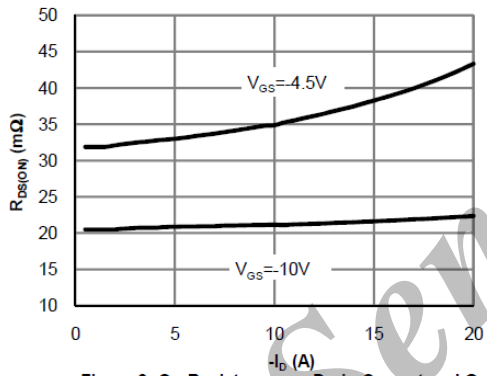


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

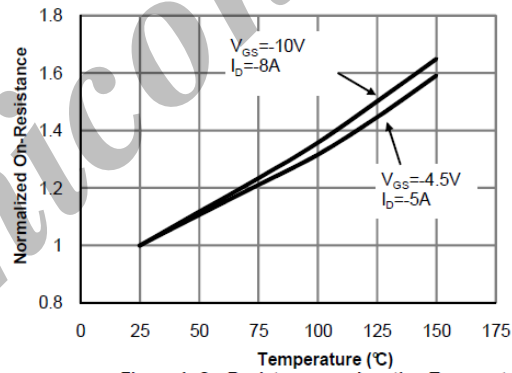


Figure 4: On-Resistance vs. Junction Temperature

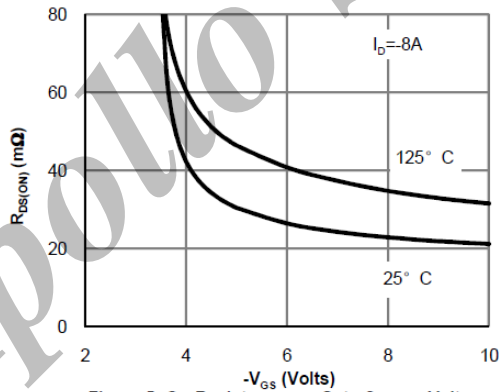


Figure 5: On-Resistance vs. Gate-Source Voltage

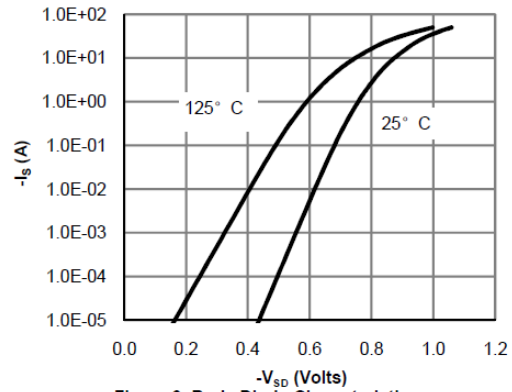


Figure 6: Body-Diode Characteristics

Note 1: The static characteristics in Figure 1 to 6 are obtained using <math>300\mu A</math> pulses, duty cycle 0.5% max.

• Typical Electrical and Thermal Characteristics

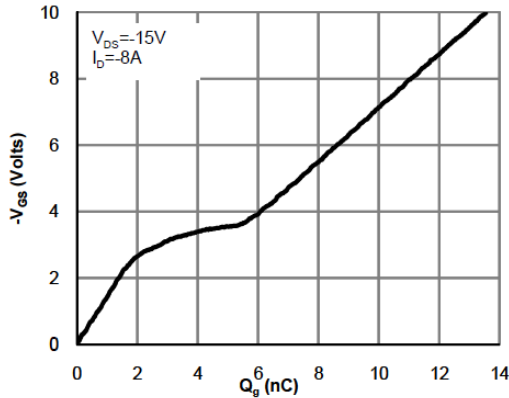


Figure 7: Gate-Charge Characteristics

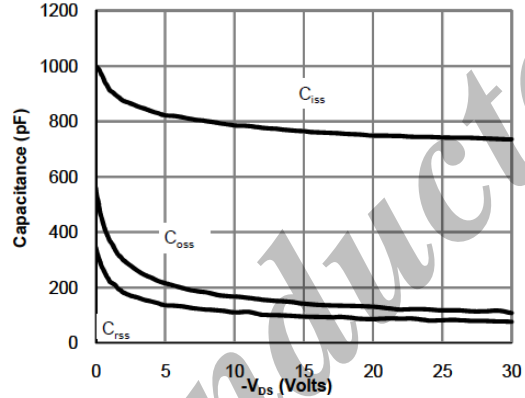


Figure 8: Capacitance Characteristics

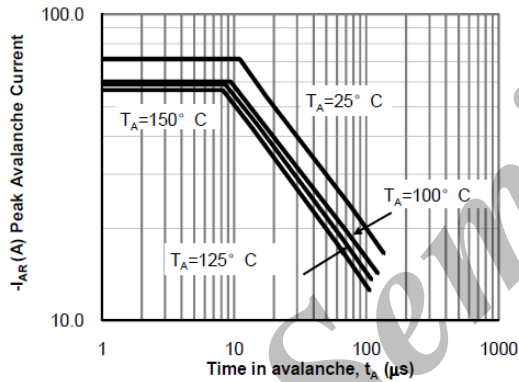


Figure 9: Single Pulse Avalanche capability

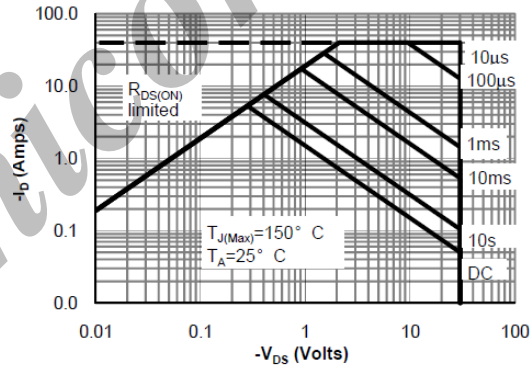


Figure 10: Maximum Forward Biased Safe Operating Area

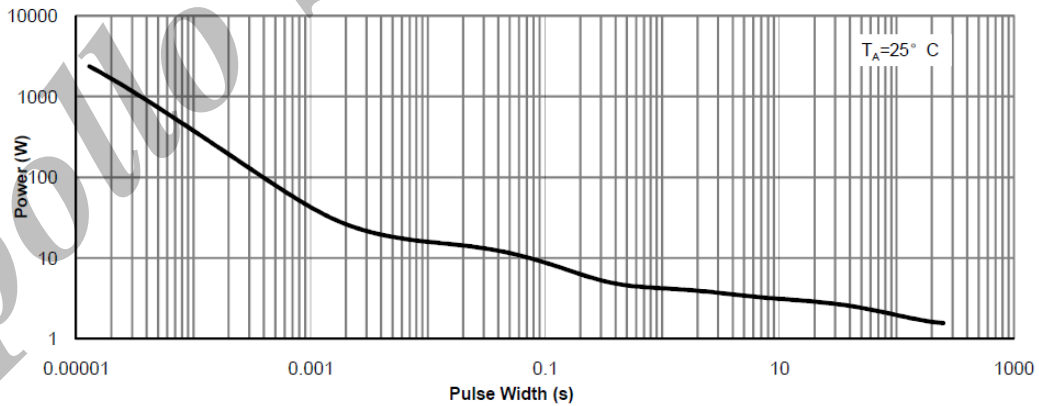


Figure 11: Single Pulse Power Rating Junction-to-Ambient

- Typical Electrical and Thermal Characteristics

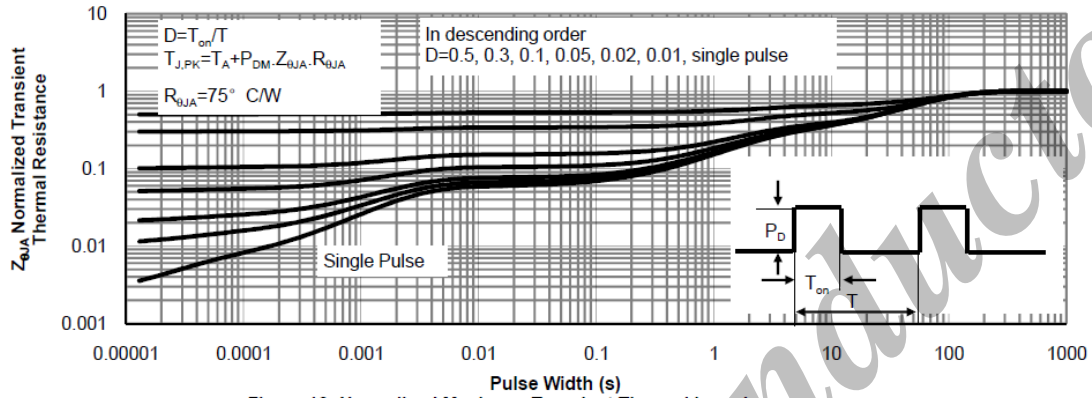


Figure 12: Normalized Maximum Transient Thermal Impedance

Note 2: The curves in Figure 10 to 12 are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. copper, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=150°C. The SOA curve provides a single pulse rating.

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