

40V N-Channel Enhancement Mode MOSFET

• General Description

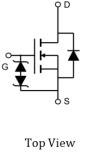
AP4480 combines advanced MOSFET technology with a low resistance package to provide extremely low $R_{DS(0N)}$. This device is ESD protected. It is most suitable for load switch or PWM applications.

Applications

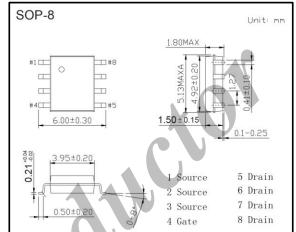
- As a low side switch in SMPS
- Load switch

Product Summary

V_{DS}	40V
In (at $V_{GS} = 10V$)	14A
$R_{DS(ON)}$ (at $V_{GS} = 10V$)	$< 11.5 \text{m}\Omega$
$R_{DS(ON)}$ (at $V_{GS} = 4.5V$)	$< 15.5 \mathrm{m}\Omega$
ESD Rating	4KV HBM









• Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage	V_{DS}	40	V		
Gate-Source Voltage	V_{GS}	±20	V		
Continuous Drain Current	Ta = 25°C	Ţ	14		
Continuous Drain Current	Ta = 70°C	I_D	11	A	
Pulsed Drain Current		I_{DM}	70		
Power Dissipation	Ta = 25°C	D	3.1	W	
	Ta = 70°C	P_D	2	VV	
Avalanche Current		I_{AR}	30	A	
Repetitive avalanche energy (L = 0.3mH)	E_{AR}	135	mJ		
Junction and Storage Temperature Range	T_J , T_{STG}	-55 to 150	°C		
Thermal Characteristics					
Thermal Resistance. Junction-to-Ambient	t ≤ 10s	$R_{ heta JA}$	40		
	Steady State	Төја	75	°C/W	
Thermal Resistance. Junction-to-Lead	Steady State	$R_{ heta JL}$	24		



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• Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Parameters						P
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250\mu A, V_{GS}=0V$	40			V
Zero Gate Voltage Drain Current	_	V_{DS} =32V, V_{GS} =0V			1	μА
	I_{DSS}	V_{DS} =32V, V_{GS} =0V, T_{J} =55°C		K	5	
Gate-Body Leakage Current	I_{GSS}	V_{DS} =0V, V_{GS} =±20V			±100	μА
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1		3	V
On-State Drain Current	I _{D(ON)}	V _{GS} =10V, V _{DS} =5V	70			A
Static Drain-Source On-Resistance		V _{GS} =10V, I _D =14A	A		11.5	mΩ
	R _{DS(ON)}	$V_{GS}=10V$, $I_D=14A$, $T_J=125$ °C		13		
		V_{GS} =4.5V, I_D =5A	1		15.5	
Forward Transconductance	$\mathbf{g}_{ ext{FS}}$	V_{DS} =5V, I_D =5A	50			S
Diode Forward Voltage	V_{SD}	$I_S=1A$, $V_{GS}=0V$			1	V
Maximum Body-Diode Continuous Current	I_S				4	Α
Dynamic Parameters						
Input Capacitance	C_{iss}			1600	1920	pF
Output Capacitance	C_{oss}	V_{GS} =0V, V_{DS} =20V, f=1MHz		320		
Reverse Transfer Capacitance	Crss			100		
Gate Resistance	Rg	V_{GS} =0V, V_{DS} =0V, f=1MHz		3.4		Ω
Switching Parameters						
Total Gate Charge (10V)				22		
Total Gate Charge (4.5V)	Qg			10.5		nC
Gate Source Charge	$Q_{\rm gs}$	V_{GS} =10V, V_{DS} =20V, I_{D} =14A		4.2		
Gate Drain Charge	Q_{gd}			4.8		
Turn-On Delay Time	t _{D(on)}			3.5		
Turn-On Rise Time	t_{r}	V_{GS} =10V, V_{DS} =20V, R_{L} =1.5 Ω ,		6		ns
Turn-Off Delay Time	$t_{\mathrm{D(off)}}$	R_{GEN} =3 Ω		13.2		
Turn-Off Fall Time	t_{f}			3.5		
Body Diode Reverse Recovery Time	t _{rr}	I_F =18A, d_i/d_t =100A/ μ s		31		
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F=18A$, $d_i/d_t=100A/\mu s$		33		nC

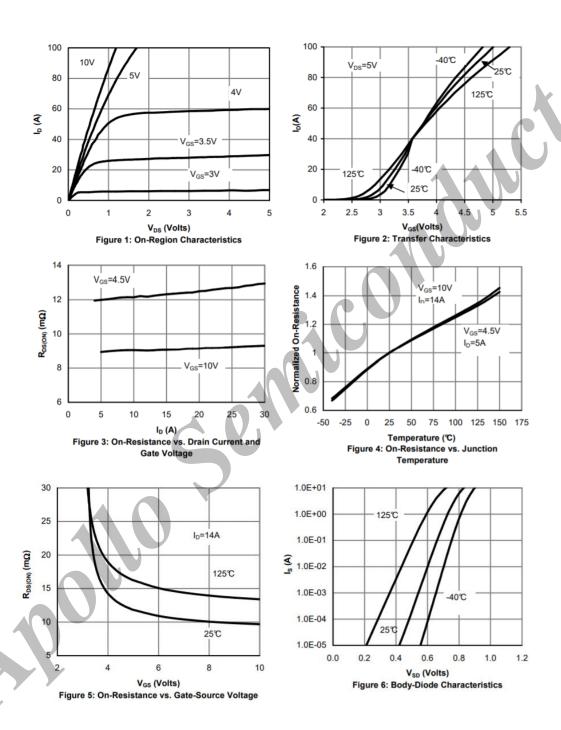
Ordering Information

Ordering Part Number	Package	MOQ
AP4480	SOP-8	2,500 pcs / reel

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• Typical Electrical and Thermal Characteristics

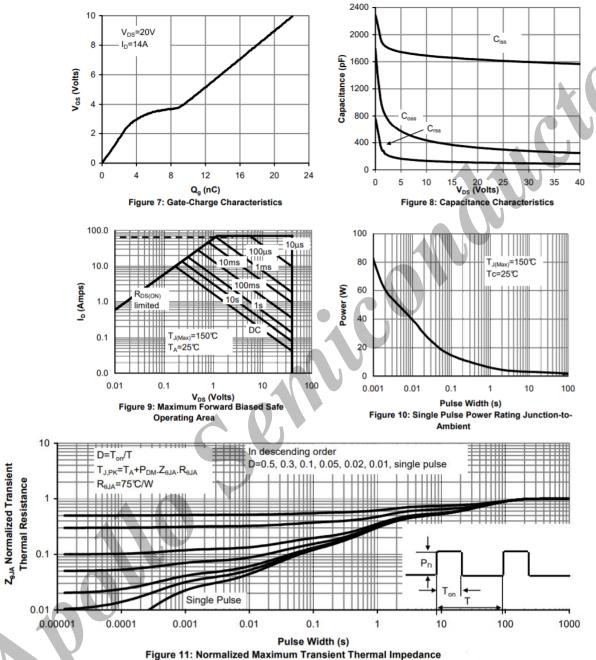


Note 1: The static characteristics in Figure 1 to 6 are obtained using $<300\mu A$ pulses, duty cycle 0.5% max.





Typical Electrical and Thermal Characteristics



Note 2: The curves in Figure 9 to 11 are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. copper, assuming a maximum junction temperature of $T_{J(MAX)}$ =150°C. The SOA curve provides a single pulse rating.



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