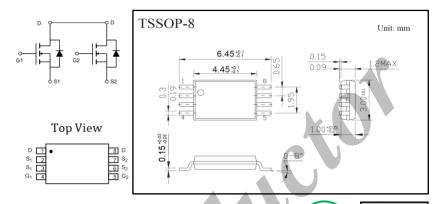


AP8205A Dual N-Channel Enhancement Mode MOSFET

• General Description

- AP8205A combines advanced MOSFET technology with a low resistance package to provide extremely low R_{DS(ON)}. This device is most suitable to Li-ion battery management applications.
- Common- Drain



Applications

Li-ion battery management





Product Summary

$V_{ extsf{DS}}$	20V
Id	6.5A
RDS(ON) (at $V_{GS} = 4.5V$)	< 25mΩ
$R_{DS(ON)}$ (at $V_{GS} = 2.5V$)	< 29mΩ

• Absolute Maximum Ratings Ta = 25°C

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±10	V
Continuous Drain Current		I_D	6.5	٨
Pulsed Drain Current		I_{DM}	20	A
Power Dissipation	Ta = 25°C	P_D	2.0	W
	Ta = 70°C		1.6] vv
Junction and Storage Temperature Range		T_J , T_{STG}	-55 to 150	°C
Thermal Characteristics				
Thermal Resistance. Junction-to-Ambient Thermal Resistance. Junction-to-Case		$R_{ heta JA}$	78	°C/W
		$R_{ heta JC}$	40	C/ W



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• Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Parameters							
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250\mu A$, $V_{GS}=0V$	20			V	
Zero Gate Voltage Drain Current	,	$V_{DS}=20V$, $V_{GS}=0V$			1		
	I_{DSS}	$V_{DS}=20V, V_{GS}=0V, T_{J}=55^{\circ}C$		K	5	μΑ	
Gate-Body Leakage Current	I_{GSS}	V_{DS} =0V, V_{GS} =±8V			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	0.5	1	1.5	V	
On-State Drain Current ** Note (a)	I _{D(ON)}	V _{GS} =4.5V, V _{DS} =5V	15			A	
Static Drain-Source On-Resistance ** Note (a)	D	V _{GS} =4.5V, I _D =6.5A		20	25	mΩ	
	R _{DS(ON)}	V _{GS} =2.5V, I _D =5.4A		23	29		
Forward Transconductance ** Note (a)	$\mathbf{g}_{ ext{FS}}$	V_{DS} =5V, I_D =3A	7	11		S	
Diode Forward Voltage	V_{SD}	I_S =1.7A, V_{GS} =0V			1.2	V	
Maximum Body-Diode Continuous Current	I_S				1.3	A	
Dynamic Parameters ** Note (b)							
Total Gate Charge	Q_{g}			7	10		
Gate Source Charge	Q_{gs}	V_{GS} =4.5V, V_{DS} =10V, I_{D} =3A		1.2		nC	
Gate Drain Charge	Q_{gd}			1.9			
Input Capacitance	C_{iss}			700			
Output Capacitance	C_{oss}	V_{GS} =0V, V_{DS} =10V, f=1MHz		175		pF	
Reverse Transfer Capacitance	C_{rss}			85			
Switching Parameters ** Note (b)							
Turn-On Delay Time	t _{D(on)}			8	16		
Turn-On Rise Time	$t_{\rm r}$	V_{DD} =10V, V_{GS} =4.5V,		10	18] _ [
Turn-Off Delay Time	$t_{D(off)}$	$I_D = 1A$, $R_{GEN} = 6\Omega$		18	29	ns	
Turn-Off Fall Time	t_{f}			5	10		

Notes.

- (a) Pulse test: $PW \le 300\mu s$, duty cycle $\le 2\%$.
- (b) For DESIGN AID ONLY, not subject to production testing.
- (c) Switching time is essentially independent of operating temperature.

Ordering Information

Ordering Part Number	Package	MOQ
AP8205A	TSSOP-8	2,500 pcs / reel

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Dual N-Channel Enhancement Mode MOSFET

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